

## Bhagwan Mahavir College of Engineering and Technology, Surat

Academic Year: 2017-18 (ODD SEM)

### CLASS TIMETABLE

DEPT : ELECTRONICS			SEM:-5		DIV:-		ROOM NO.706C	
Period	1	2	3	4		5	6	7
Day / Time	8:30 TO 9:30	9:30 TO 10:30	10:30 TO 11:30	11:30 TO 12:30	12:30 TO 1:00	1:00 TO 2:00	2:00 TO 3:00	3:00 TO 4:00
<b>Mon Day</b>		ECOMM(UP)	MCI(PP)	DD(RG) (707C)	<b>RECESS</b>	CYS(C1)(SK)(606B2)		
<b>Tues Day</b>	ECOMM(UP)	EMAG(JD)	MCI(PP)	DD(RG) (707C)		ECOMM(C1)(UP)(708C)		
<b>Wed Day</b>	EMAG(JD)	ECOMM(UP)	MCI(PP)	DD(RG) (707C)		EMAG(C1)(JD)(710C)		
<b>Thurs Day</b>	DD(RG)	MCI(PP)	ECOMM(UP)	EMAG(JD)		MINI PROJECT		
<b>Fri Day</b>	DD(C1)(RG)(606B2)		CYS(T)(SK)	EMAG(JD)		MCI(C1)(PP)(606B2)		
<b>Satur Day</b>	DE-II A(VP,JD) (706C)		DE-II A(VP,JD) (706C)			DE-II A(VP,JD) (706C)		

**ABBREVIATIONS & ACRONYMS :**

**SUB**

SUB		FACULTY	
EMAG	ENGINEERING ELECTROMAGNETICS	JD	MR.JUGAL DESAI
MCI	MICROCONTROLLER AND INTERFACING	PP	MR.PRATIK PASIYAWALA
DD	DIGITAL DESIGN	RG	MR.RAHUL GONAWALA
ECOMM	ELECTRONICS COMMUNICATION	UP	MR.UDAY PATEL
CYS	CYBER SECURITY	SK	MR.SATISH KEDAR
DE-II B	DESIGN ENGG.	VP,JD	MR. VIRENDRA PATEL ,MR. JUGAL DESAI

Department Timetable I/C  
MR. UDAY PATEL, MR. SATISH KEDAR

College Timetable I/C  
MR.SUNIL JANKAR

HOD  
MRS. HIRAL PATEL

DIRECTOR  
Dr. R.R.SAWANT