

Bhagwan Mahavir College of Engineering and Technology, Surat

Academic Year: 2017 (EVEN SEM)

CLASS TIMETABLE

DEPT : ELECTRONICS		SEM:-6			DIV:-		ROOM NO.706C,707C	
Period	1	2	3		4	5	6	7
Day / Time	8:30 TO 9:30	9:30 TO 10:30	10:30 TO 11:30	11:30 TO 12:00	12:00 TO 1:00	1:00 TO 2:00	2:00 TO 3:00	3:00 TO 4:00
Mon Day	TSSN(PS)(707C)	DC(B1)(PB)(606B2)		RECESS	DCOMM(AS) (706 C)	AWP(JD) (706C)	PEDC(VM) (707C)	VD(RG) (707C)
Tues Day	TSSN(PS)(707C)	PEDC(B1)(VM)(709B)			DCOMM(AS) (706 C)	DC(PB) (707C)	VD(RG) (707C)	AWP(JD) (706C)
Wed Day	TSSN(PS)(707C)	DCOMM(B1)(AS)(708B)			DC(PB) (707C)	AWP(JD) (706C)	DCOMM(AS) (706 C)	PEDC(VM) (707C)
Thurs Day	TSSN(PS)(707C) VD(RG)(706C)	AWP(B1)(PB)(709B)			PEDC(VM) (707C)	DC(PB) (707C)	TSSN(T1)PS (604B) VD(B1)(RG)(606B2)	
Fri Day	DC(PB) (707C)	DCOMM(AS) (706 C)	AWP(JD) (706C)		VD(RG) (707C)	STUDENT ACTIVITY		
Satur Day	DE-II B(PP) (706C)				DE-II B(PP) (706C)			

ABBREVIATIONS & ACRONYMS :

SUB

		FACULTY	
TSSN	TELECOMMUNICATION SWITCHING N/W	PS	MR.PARESH SOLANKI
VD	VLSI DESIGN	RG	MR.RAHUL GONAWALA
AWP	ANTENNA AND WAVE PROPAGATION	JD	MR.JUGAL DESAI
DCOM	DIGITAL COMMUNICATION	AS	MR.ANKIT SOLANKI
PEDC	POWER ELECTRONICS DEVICE CIRCUIT	VM	MR.VIRAL MAKWANA
DC	DIGITAL CONTROL	PB	MS.PRITI BHAGAT
DE-II B	DESIGN ENGG.	PP	MR. PRATIK PASIYAWALA

Department Timetable I/C
MR. UDAY PATEL, MR. SATISH KEDAR

College Timetable I/C
SUNIL JANKAR

HOD
MRS. HIRAL PATEL

DIRECTOR
Dr. R.R.SAWANT